

FIG.1A

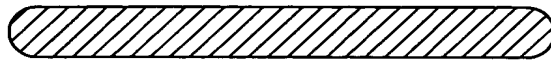


FIG.1B

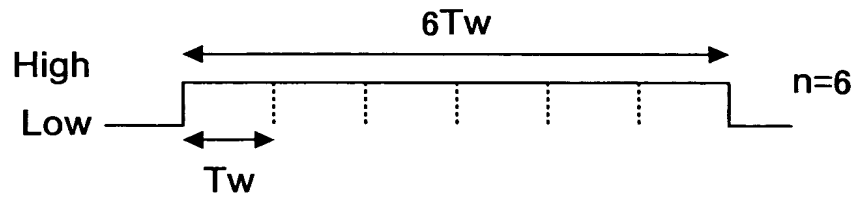


FIG.2

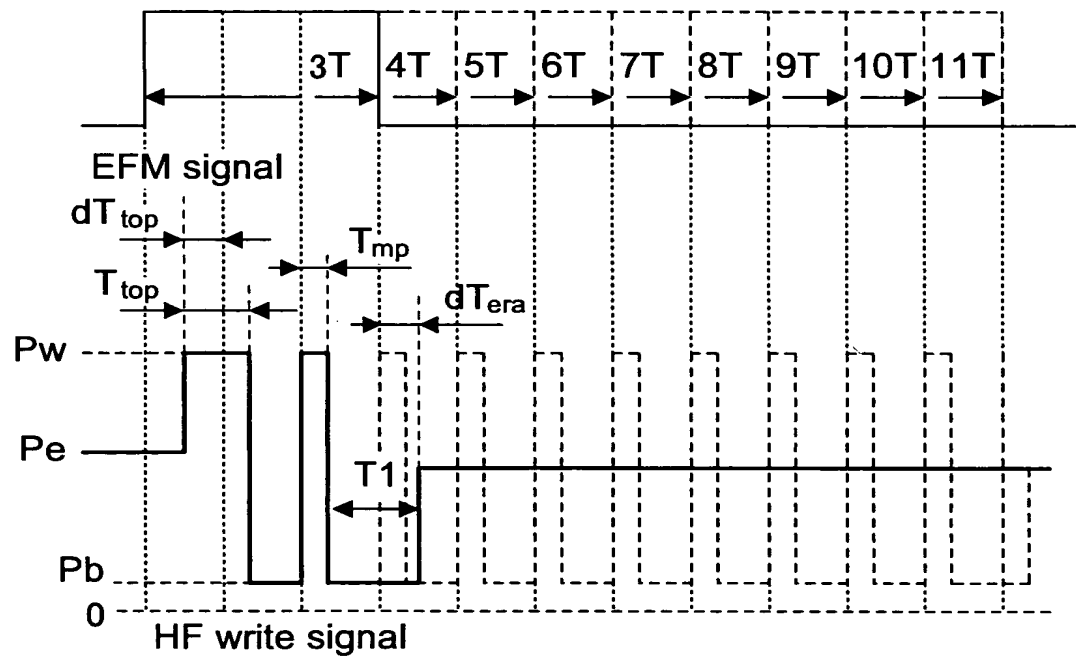


FIG.3

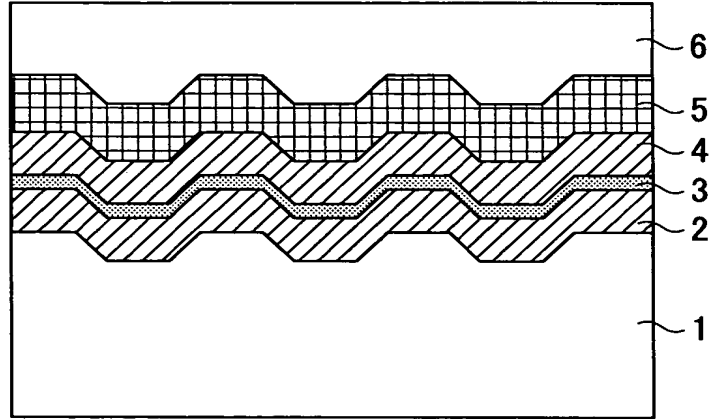


FIG. 4

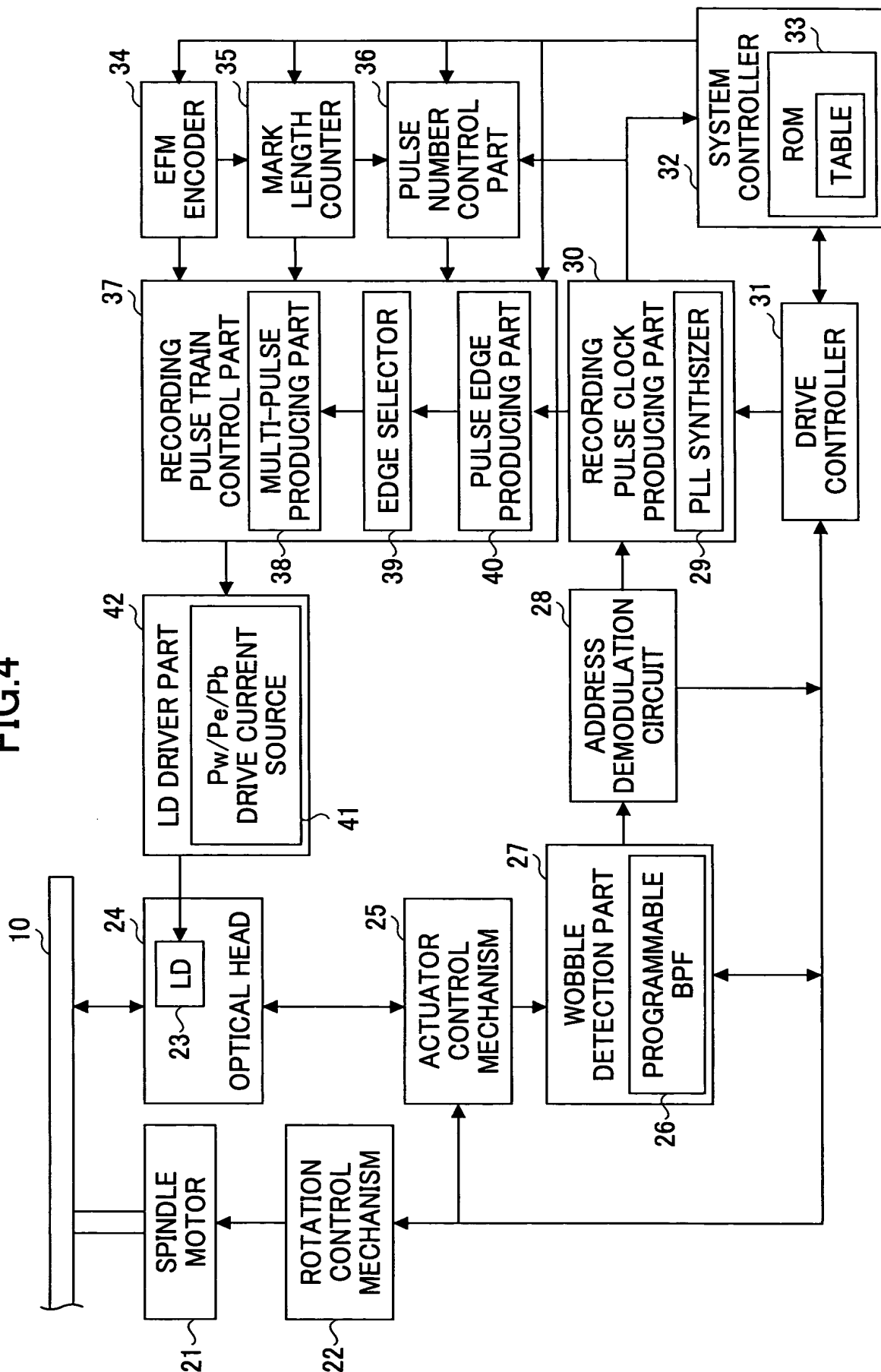


FIG.5

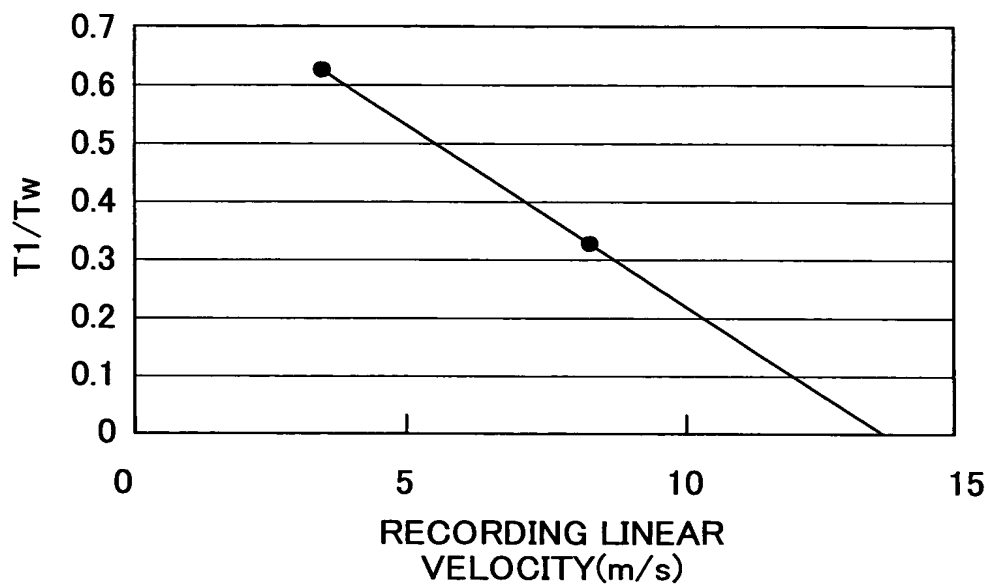


FIG.6A

dTtop-Table

bit4~bit7	dTtop/T
0000	0.1
0001	0.0
0010	-0.1
0011	-0.2
0100	-0.3
0101~1111	Reserved

FIG.6B

Ttop-Table

bit4~bit7	Ttop(ns)
0000	4.0
0001	4.5
0010	5.0
0011	5.5
0100	6.0
0101	6.5
0110	7.0
1111	7.5
1000~1111	Reserved

FIG.7A

Tmp-Table

bit4~bit7	Tmp(ns)
0000	Reserved
0001	2
0010	3
0011	4
0100	5
0101	6
0110	7
0111~1111	Reserved

FIG.7B

dTera-Table

bit4~bit7	dTera(ns)
0000	0
0001	1
0010	2
0011	3
0100	4
0101	5
0110	6
0111	7
1000	8
1001~1111	Reserved